## RABIN & BERDO, P.C.

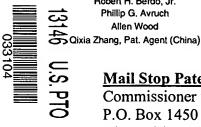
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In Affiliation with Weiss, Moy & Harris, PC having offices in Arizona and Nevada and in Washington, DC with Rabin & Berdo, PC

Attorney Reference: OKI 417

March 31, 2004



## Mail Stop Patent Application

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

New Patent Application of: Seiichiro SASAKI and Kouji MORITA

Title: MULTILAYERED POWER SUPPLY LINE FOR SEMICONDUCTOR

INTEGRATED CIRCUIT AND LAYOUT METHOD THEREOF

Sir:

Steven M. Rabin

Robert H. Berdo, Jr.

Phillip G. Avruch

Allen Wood

Please find attached hereto an application for patent which includes:

- Specification, Claims and Abstract (24 Pages)
- [x]5 Sheets of Formal Drawings (Fig. 1 through Fig. 13)
- Inventor's Declaration and Power of Attorney (3 Pages) [x]
- Assignment document with cover page (3 Pages) [x]
- Claim for priority and the Priority document [x]
- Information Disclosure Statement, Form PTO-1449, and copies of 2 [x]references
- [x] Form PTO-2038
- Fee (see formula below) [x]

Basic Fee \$770/385 Additional Fees:	\$ _	770.00
Total number of claims: <u>15</u> in excess of 20: <u>0</u> times \$18/9	<b>\$</b> .	0.00
Number of independent claims: 3 in excess of 3: 0 times \$86/43	\$	0.00
Multiple Dependent Claims \$290/145	\$	0.00
Recording Fee \$40.	\$_	40.00
TOTAL FEES FOR THE ABOVE APPLICATION	\$	810.00

In the event there is attached hereto no credit card form PTO-2038 or check, or there is an insufficient amount, please charge the fee to our Account No. 18-0002 and notify us accordingly.

The rights of priority are claimed under 35 USC §119 of Japanese Application No. 2003-397724, filed November 27, 2003.

Robert H. Berdo, Jr. (Reg. No. 38,075)

(Customer No. 23995)

Respectfully submitte

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RHB:tz